

Job Description

JOB TITLE: Graduate FPGA Engineer (Research & Development)	LOCATION: Blandford
RESPONSIBLE TO: Project/Delivery Manager, Team Leader or appointed Senior Engineer	RESPONSIBLE FOR: No direct reports
ASSOCIATED JOB GRADE(S): T1 – E5	DATE OF JOB DESCRIPTION REVIEW: July 2011
MAIN PURPOSE(S) OF JOB: To work under the supervision of the Team Leader or TL appointed engineer to set up systems, test components or systems, integrate systems, replicate faults and develop robust and reliable FPGA cores upon agreed requirements and design specifications to planned delivery timescales. Assist in schematic capture/design of new hardware projects. <u>Achieved through:-</u> Training and supervision on how systems and components function and should operate. Training on Telesoft Technologies' processes and procedures. Understanding the system requirements, translating these into a design and validating that the design implementation meets these requirements. Developing code in line with the agreed design specifications within the guidelines of Telesoft Technologies' FPGA development processes. Confirming through planned and adequate component testing that any component developed will meet the agreed system requirements.	
DUTIES, TASKS AND ACTIVITIES PERFORMED IN THE JOB: Setting up systems including the connection of test equipment. Producing design documents under supervision for components being developed based upon agreed requirements, system design specifications and relevant standards specifications. Implementing of design in VHDL. Compliance with Telesoft Processes and Procedures to ensure that ISO 9001 and Tick-IT Requirements are met. Attending code reviews to verify implementation. Using software source code control systems in to manage software version control. Produce test benches and simulations to prove implementation meets requirements. Use Synthesis and Place-and-route tools, and 'Identify' debugging tool. Producing professional documentation in line with Telesoft Technologies' processes, including Release Notes,	

Change Notes, BTRDs and User Guides as appropriate.

Promptly correcting any deficiencies against Requirements identified either internally through Component or System Testing or externally by Customer.

Use SAP system for Quality Management through Customer Service Notifications and Project Management through Timesheets recording.

Attend meetings with other teams to agree customer requirements.

Attend customer site for installation or fault diagnosis of systems.

KEY KNOWLEDGE AREAS / QUALIFICATIONS:

Relevant degree such as electronics engineering.

Understanding of basic digital telecommunications.

Experience in programming languages VHDL or Verilog.

Good understanding of System Architecture philosophies.

Ability to understand, apply and review Telesoft Processes to ensure they remain valid in meeting the objectives set without unnecessary complication/bureaucracy.

Ability to write design documentation and understand the use of message sequence charts and SDLs

Experience with different operating systems including UNIX and Windows.

Understanding of the methods of testing software.

KEY INTERFACES:

Engineering Team Leader, Systems Integration Team, Systems Proving Team, Technical Support Team, Delivery Managers, Engineers, and customers.